



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Vernon M. Williams et al.

Serial No.: 09/590,646

Filed: June 8, 2000

For: REINFORCED, SELF-ALIGNING
CONDUCTIVE STRUCTURES FOR
SEMICONDUCTOR DEVICE
COMPONENTS AND METHODS FOR
FABRICATING SAME

Examiner: Unknown

Group Art Unit: 2825

Attorney Docket No.: 4210US (99-0173)

CERTIFICATE OF MAILING

I hereby certify that this paper or fee along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail (under 37 C.F.R. § 1.8(a)) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

January 3, 2002
Date of Deposit

Signature of registered practitioner or other person
having reasonable basis to expect mailing to occur
on date of deposit shown pursuant to 37 C.F.R. §
1.8(a)(1)(ii)

Deidra J. Pfeil
Typed/printed name of person whose signature is
contained above

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the document listed on attached Form PTO-1449 be considered by the Examiner and made of record. A copy of the listed document is enclosed pursuant to 37 C.F.R. § 1.98(a).

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as

defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

DOCUMENTS

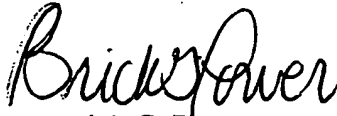
U.S. Patent Documents

<u>U.S. Patent No.</u>	<u>Issue Date</u>	<u>Inventor</u>
5,484,314	01/1996	Farnworth

Applicants offer to supply any explanation or discussion of the document which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits, and therefore no fee is due.

Respectfully submitted,



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BGP/ps:djp

Enclosures: Form PTO-1449
Copy of document cited

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THE PATENT & TRADEMARK OFFICE MAILROOM DATE STAMPED
HEREON IS AN ACKNOWLEDGEMENT THAT ON THIS DATE THE
PATENT & TRADEMARK OFFICE RECEIVED:

Supplemental Information Disclosure Statement (2 pages), and Form PTO-1449
(1 page), with copy of cited reference

Invention: REINFORCED, SELF-ALIGNING CONDUCTIVE STRUCTURES
FOR SEMICONDUCTOR DEVICE COMPONENTS AND METHODS FOR
FABRICATING SAME

Applicant(s): Vernon M. Williams et al.

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